

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) An electronic circuit ~~according to the invention~~ comprising:

a first substrate including a first coil that is formed by wiring on the first a substrate, and is connected to a transmitter circuit; and

a second substrate including a second coil that is formed by wiring on the second a substrate at a position corresponding to said first coil, and is inductively coupled to said first coil, and is connected to a first receiver circuit; and

a third substrate including a third coil that is formed by wiring on the third substrate at a position corresponding to said first and second coils, is inductively coupled to said first and second coils, and is connected to a second receiver circuit;

said substrates being laminated in order from said first substrate to said third substrate,

wherein said transmitter circuit changes a potential of one end of said first coil from a first potential to a second potential in response to a change of a transmission digital signal from zero to one, changes a potential of the other end of said first coil from said first potential to said second potential in a predetermined delay, changes a potential of said one end of said first coil from said second potential to said first potential in response to a change of said transmission digital signal from one to zero, and changes a potential of said other end of said first coil from said second potential to said first potential in a predetermined delay; and

said first and second receiver circuits receive a digital signal transmitted by said transmitter circuit.

2. (Canceled)

3. (Currently Amended) The electronic circuit according to Claim 1, wherein said first and second substrate further includes a receiver circuit circuits further include two resistors that respectively connect ~~connects~~ both ends of said first and second coil coils to one ~~[[a]] predetermined voltage source via resistors.~~

4. (Currently Amended) The electronic circuit according to Claim 1, wherein said first coil is smaller than inductively coupled to said second and third coils of a plurality of said second substrate.

5. (Canceled)

6. (Canceled)

7. (Canceled)

8. (New) An electronic circuit comprising:

a first substrate including a first coil that is formed by wiring on the first substrate, and is connected to a transmitter circuit;

a second substrate including a second coil that is formed by wiring on the second substrate at a position corresponding to said first coil, is inductively coupled to said first coil, and is connected to a first receiver circuit; and

a third substrate including a third coil that is formed by wiring on the third substrate at a position corresponding to said first and second coils, is inductively coupled to said first and second coils, and is connected to a second receiver circuit,

said substrates being laminated in order from said first substrate to said third substrate,

wherein said transmitter circuit connects one end of said first coil to two potentials selectively in response to a transmission digital signal and maintains the other end of said first coil in an intermediate potential between said two potentials; and

said first and second receiver circuits receive a digital signal transmitted by said transmitter circuit.

9. (New) The electronic circuit according to Claim 8, wherein said first and second receiver circuits further include two resistors that respectively connect both ends of said first and second coils to one predetermined voltage source.

10. (New) The electronic circuit according to Claim 8, wherein said first coil is smaller than said second and third coils.